



Docket No.: 12-1128

GP 2123
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IN THE U.S. PATENT AND TRADEMARK OFFICE

CERTIFICATE OF MAILING

Application No.: 09/840,500
Filing Date: April 23, 2001
Inventor(s): Roger S. Tsai
Group Art Unit: 2123
Examiner Name: Stevens, Thomas H.
Title: Process Perturbation to Measured-
Modeled Method for Semiconductor
Device Technology Modeling

I hereby certify that this paper is being deposited with the
United States Postal Service as first-class mail in an envelope
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date.

1/6/05

Date

Sally J. Navratil
SALLY J. NAVRATIL

Customer No.: 27160

Confirmation No.: 4458

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Official Action mailed on August 6, 2004, please enter the following
Amendment:

Amendments to the Claims are reflected in the listing of claims which begins on page 2
of this paper.

Remarks/Arguments begin on page 4 of this paper.